Docket: 0756-1950

In the claims:

Please amend claims 1, 9, 13, 21, 25, 33, 37 and 45 as follows:

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1. (Amended) A device having at least one liquid crystal panel, said liquid crystal panel comprising:

a first substrate having an insulating surface;

a second substrate being opposed to the first substrate;

at least one thin film transistor being formed over the first substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

wherein the channel region, the source and drain region of said one thin film transistor is formed in a [crystalline] semiconductor island;

an organic resin film formed over said first substrate to provide a leveled upper surface over said first substrate, said organic resin film covering said thin film transistor;

a pixel electrode formed on said leveled upper surface, said pixel electrode being electrically connected to said thin film transistor through an opening formed in said organic resin film;

a liquid crystal material having ferroelectricity or anti-ferroelectricity and being formed between the first substrate and the second substrate.

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9. (Amended) A device having at least one liquid crystal panel, said liquid crystal panel comprising:

a first substrate having an insulating surface;

a second substrate being opposed to the first substrate;

at least one thin film transistor being formed over the first substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

wherein the channel region, the source and drain region of said one thin film transistor is formed in a [crystalline] semiconductor island;

an organic resin film formed over said first substrate to provide a leveled upper surface over said first substrate, said organic resin film covering said thin film transistor;

a pixel electrode formed on said leveled upper surface, said pixel electrode being electrically connected to said thin film transistor through an opening formed in said organic resin film;

a liquid crystal material having ferroelectricity or anti-ferroelectricity and being formed between the first substrate and the second substrate;

a leveling film being formed over said second substrate;

an opposed electrode formed over said leveling film and opposed to said pixel electrode with said liquid crystal material interposed therebetween.

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13. (Amended) A television comprising:

a tuner for receiving television radio wave;

a liquid crystal panel operationally connected to said tuner, said liquid crystal panel comprising:

a first substrate having an insulating surface;

a second substrate being opposed to the first substrate;

at least one thin film transistor being formed over the first substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region and

a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

wherein the channel region, the source and drain region of said one thin film transistor is formed in a [crystalline] semiconductor island;

an organic resin film formed over said first substrate to provide a leveled upper surface over said first substrate, said organic resin film covering said thin film transistor;

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a pixel electrode formed on said leveled upper surface, said pixel electrode being electrically connected to said thin film transistor through an opening formed in said organic resin film;

a liquid crystal material having ferroelectricity or anti-ferroelectricity and being formed between the first substrate and the second substrate.

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21. (Amended) A television comprising:

a tuner for receiving television radio wave;

a liquid crystal panel operationally connected to said tuner, said liquid crystal panel comprising:

a first substrate having an insulating surface;

a second substrate being opposed to the first substrate;

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at least one thin film transistor being formed over the first substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

wherein the channel region, the source and drain region of said one thin film transistor is formed in a [crystalline] semiconductor island;

an organic resin film formed over said first substrate to provide a leveled upper surface over said first substrate, said organic resin film covering said thin film transistor;

a pixel electrode formed on said leveled upper surface, said pixel electrode being electrically connected to said thin film transistor through an opening formed in said organic resin film;

a liquid crystal material having ferroelectricity or anti-ferroelectricity and being formed between the first substrate and the second substrate;

a leveling film being formed over said second substrate;

an opposed electrode formed over said leveling film and opposed to said pixel electrode with said liquid crystal material interposed therebetween.

25. (Amended) A portable computer having a liquid crystal panel, said liquid crystal panel comprising:

a first substrate having an insulating surface;

a second substrate being opposed to the first substrate;

at least one thin film transistor being formed over the first substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

wherein the channel region, the source and drain region of said one thin film transistor is formed in a [crystalline] semiconductor island;

an organic resin film formed over said first substrate to provide a leveled upper surface over said first substrate, said organic resin film covering said thin film transistor;

a pixel electrode formed on said leveled upper surface, said pixel electrode being electrically connected to said thin film transistor through an opening formed in said organic resin film;

a liquid crystal material having ferroelectricity or anti-ferroelectricity and being formed between the first substrate and the second substrate.

33. (Amended) A portable computer having a liquid crystal panel, said liquid crystal panel comprising:

a first substrate having an insulating surface;

a second substrate being opposed to the first substrate;

at least one thin film transistor being formed over the first substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

wherein the channel region, the source and drain region of said one thin film transistor is formed in a [crystalline] semiconductor island;

an organic resin film formed over said first substrate to provide a leveled upper surface over said first substrate, said organic resin film covering said thin film transistor;

a pixel electrode formed on said leveled upper surface, said pixel electrode being electrically connected to said thin film transistor through an opening formed in said organic resin film;

a liquid crystal material having ferroelectricity or anti-ferroelectricity and being formed between the first substrate and the second substrate;

a leveling film being formed over said second substrate;

an opposed electrode formed over said leveling film and opposed to said pixel electrode with said liquid crystal material interposed therebetween.

37. (Amended) A projector comprising:

a light source;

at least one liquid crystal panel to modify the light from said light source;

at least one lens for projecting the light modified by said one liquid crystal panel onto a screen, wherein said liquid crystal panel comprises:

a first substrate having an insulating surface;

a second substrate being opposed to the first substrate;

at least one thin film transistor being formed over the first substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

wherein the channel region, the source and drain region of said one thin film transistor is formed in a [crystalline] semiconductor island;

an organic resin film formed over said first substrate to provide a leveled upper surface over said first substrate, said organic resin film covering said thin film transistor;

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a pixel electrode formed on said leveled upper surface, said pixel electrode being electrically connected to said thin film transistor through an opening formed in said organic resin film;

a liquid crystal material having ferroelectricity or anti-ferroelectricity and being formed between the first substrate and the second substrate.

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45. (Amended) A projector comprising:

a light source;

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at least one liquid crystal panel to modify the light from said light source;

at least one lens for projecting the light modified by said one liquid crystal panel onto a screen, wherein said liquid crystal panel comprises:

a first substrate having an insulating surface;

a second substrate being opposed to the first substrate;

at least one thin film transistor being formed over the first substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

wherein the channel region, the source and drain region of said one thin film transistor is formed in a [crystalline] semiconductor island;

an organic resin film formed over said first substrate to provide a leveled upper surface over said first substrate, said organic resin film covering said thin film transistor;

a pixel electrode formed on said leveled upper surface, said pixel electrode being electrically connected to said thin film transistor through an opening formed in said organic resin film;

a liquid crystal material having ferroelectricity or anti-ferroelectricity and being formed between the first substrate and the second substrate;

a leveling film being formed over said second substrate;

an opposed electrode formed over said leveling film and opposed to said pixel electrode with said liquid crystal material interposed therebetween.

Please add new claims 49-56 as follows:

--49. A device according to claim 1, wherein the semiconductor island is a crystalline semiconductor island.

50. A device according to claim 9, wherein the semiconductor island is a crystalline semiconductor island.

51. A television according to claim 13, wherein the semiconductor island is a crystalline semiconductor island.

52. A television according to claim 21, wherein the semiconductor island is a crystalline semiconductor island.

53. A portable computer according to claim 25, wherein the semiconductor island is a crystalline semiconductor island.

54. A portable computer according to claim 33, wherein the semiconductor island is a crystalline semiconductor island.

55. A projector according to claim 37, wherein the semiconductor island is a crystalline semiconductor island.

56. A projector according to claim 45, wherein the semiconductor island is a crystalline semiconductor island.

REMARKS

The Examiner's Official Action dated June 30, 1999 has been received and its contents carefully noted. Filed concurrently herewith is a *Request for a One Month Extension of Time* which extends the shortened statutory period for response to November 1, 1999. Accordingly, applicant respectfully submits that this response is being timely filed.

Claims 1-48 were pending in the present application prior to the above amendment. Claims 1, 9, 13, 21, 25, 33, 37 and 45 have been amended and new claims 49-56 have been added to recite additional protection to which applicant is entitled. Accordingly,